



## **SAMARA TECHNOLOGY GROUP ENABLES THE BERKELEY UPC COMPILER TO USE CUTTING EDGE INTERCONNECT TECHNOLOGY**

### **Optimizing Next Generation Parallel Programming Models**

**August 1, 2011 Boxford, Massachusetts** –Samara Technology Group, LLC, a leader in high performance optimization and development announced today the availability of an open source GASNet layer for Berkeley’s UPC compiler over Cray’s Gemini interconnect.

GASNet is a language-independent, low-level networking layer that provides network-independent, high-performance communication primitives.

Samara was approached by NERSC (National Energy Research Scientific Computing) to perform the port and worked closely with engineers from Cray to tune the library for optimal performance.

According to Philip Mucci, Samara's Director of Business Development, “We were able to achieve more than double the bandwidth and nearly half the latency of the reference GASNet built on top of MPI. Outstanding performance on GASNet is critical for application scaling not just on UPC, but also for Cray's Chapel compiler.”

For more information on Berkeley’s UPC compiler, visit <http://upc.lbl.gov>.

For more information on NERSC, visit <http://www.nersc.gov>.

### **About Samara Technology Group, LLC**

Samara Technology Group, LLC specializes in delivering performance technology and consulting on platforms ranging from embedded platforms to massively parallel petaflop scale supercomputers. The company was founded by a small group of senior consultants, application engineers, managers and software architects with well over 100 years of experience in the field of computer performance. Clients range from small technology startups, to major computer system vendors, national laboratories and universities around the globe.

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